



Operating Note

68020/68EC020
PGA to PGA Adapter



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Introduction

The 68020/68EC020 PGA to PGA Adapter (part number 64748-87602) converts a 100-pin 68EC020 PGA pattern to a 114-pin 68020 PGA pattern. The PGA pin locations for the 68EC020 signals remain the same on the 114-pin side of the adapter; the additional 14 pins are not connected.

The PGA to PGA Adapter allows emulators or preprocessor interfaces with 68020 probes to be attached to 68EC020 target systems. The emulator or preprocessor interface can then use 68EC020 software to analyze 68EC020 target systems.

Installation

Note

The PGA to PGA Adapter comes with a pin protector attached to the target system end. The pin protector should be left attached to the adapter, as it protects the pins on the PGA to PGA Adapter. If additional protectors/extenders are required, they may be ordered from McKenzie Technology (part number 100H004B1-13F4), or from equivalent sources. However, pin protectors do add additional capacitive loading.

The location of pin A1 is the same for both the 68020 side and the 68EC020 side. It is marked with the grid designations, and with a chamfered corner (see illustration). Position the adapter on the 68EC020 PGA microprocessor socket on your target system. Ensure that pin A1 is properly aligned. Gently press down on the adapter until all pins are firmly seated.

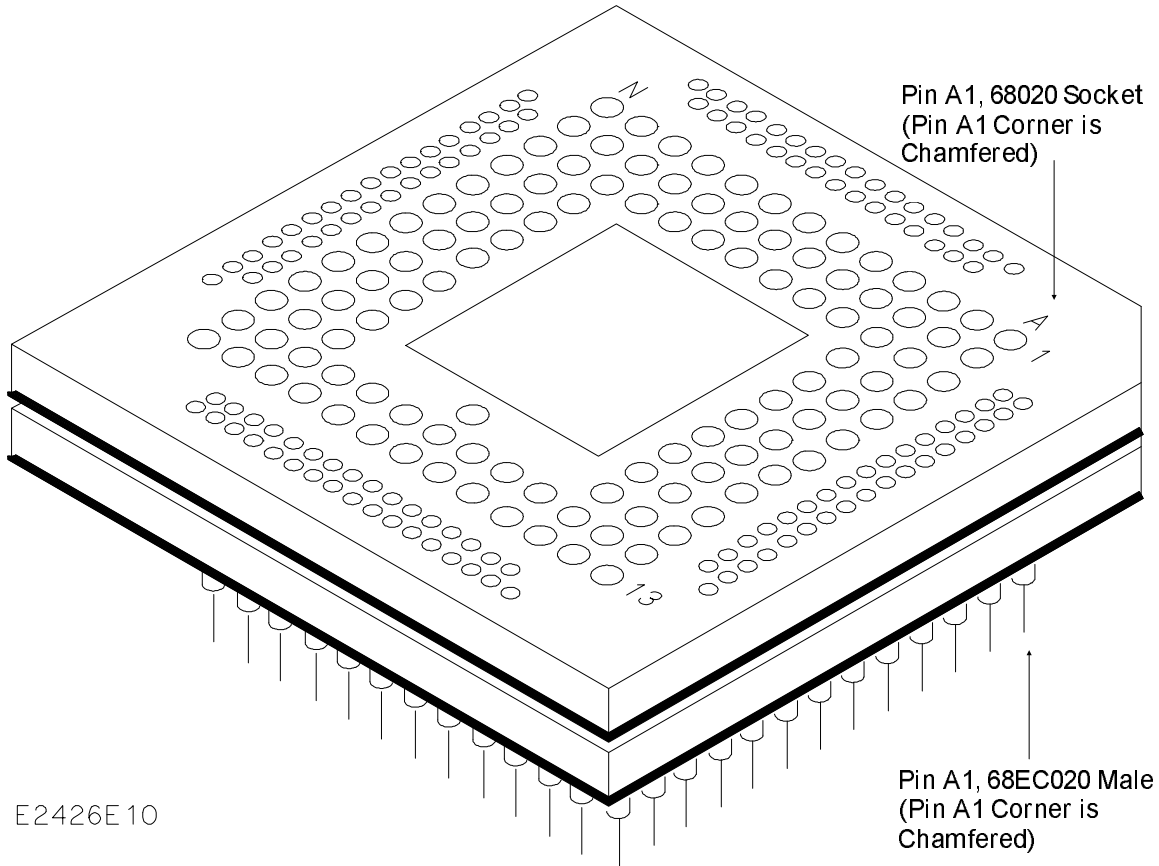
Caution

Incorrect alignment of pin A1 will cause damage to the adapter, as the pin pattern is not symmetrical..

Position your emulator or preprocessor interface on the 68020 end of the adapter. Ensure that pin A1 is properly aligned. Gently press down until all pins are firmly seated.

Removal

When removing the emulator/preprocessor interface or adapter, gently pull the probe or adapter straight up. Do not apply lateral force, as that might damage the PGA pins on the emulator/ preprocessor interface or adapter.



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Pin A1, 68020 Socket
(Pin A1 Corner is Chamfered)

Pin A1, 68EC020 Male
(Pin A1 Corner is Chamfered)